

Satisfiability Checking

SMT Applications

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Informatik 2
LuFG Theory of Hybrid Systems

WS 14/15

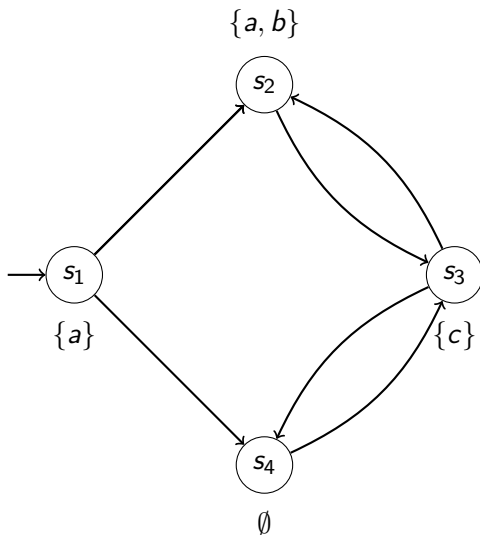
1 Bounded model checking

2 Deductive verification

3 Schere, Stein, Papier

4 Hardware design

Kripke structures



Definition

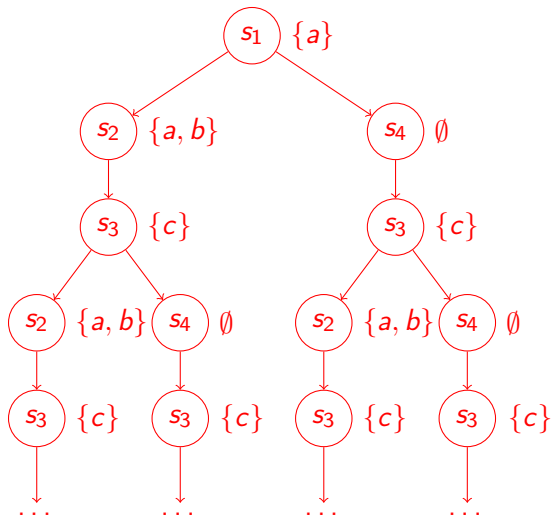
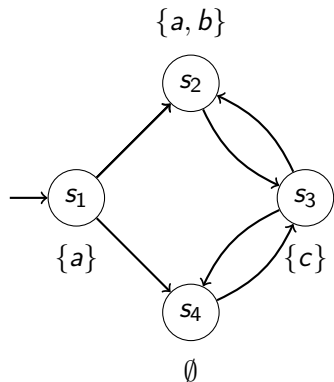
Let AP be a finite set of atomic propositions. A **Kripke structure** is a tuple $M = (S, s_{\text{init}}, T, L)$ with

- S a finite set of **states**,
- $s_{\text{init}} \in S$ an **initial state**,
- $T \subseteq S \times S$ a **transition relation**,
- $L : S \rightarrow 2^{AP}$ a **labeling function**
(2^{AP} denotes the powerset over AP).

The labeling function attaches information to the system: for a state $s \in S$ the set $L(s)$ consists of those atomic propositions that hold in s .

- An **(infinite) path** $\pi = s_0 s_1 s_2 \dots$ of a Kripke structure $M = (S, s_{\text{init}}, T, L)$ is a sequence of states such that
 - $s_0 = s_{\text{init}}$ and
 - $(s_i, s_{i+1}) \in T$ for all $i \geq 0$.
- The **behaviour** of M is given by the set of all of its infinite paths.
- A **finite path** of M is a finite prefix of an infinite path of M .
- For a finite path $\pi = s_0 \dots s_k$ we define $|\pi| = k$.
- We write $\pi(j)$ for the j th state (starting with 0) of the path π .
- By π_j we denote the postfix of π starting at $\pi(j)$.

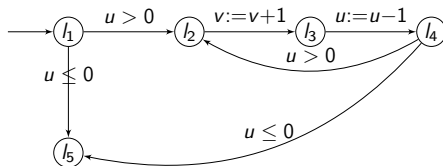
Kripke structure: Semantics



```
    m(int u, int v){  
l1:  while (u>0) do  
l2:    v:=v+1;  
l3:    u:=u-1;  
l4:  od  
l5:  return v  
    }
```

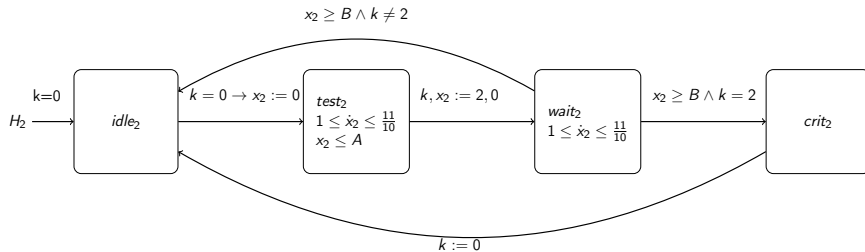
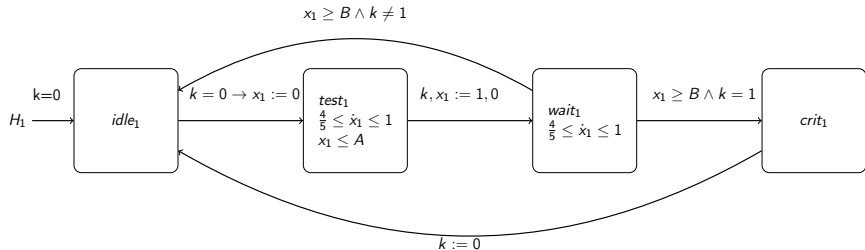
Transition systems

```
m(int u, int v){  
l1: while (u>0) do  
l2:   v:=v+1;  
l3:   u:=u-1;  
l4: od  
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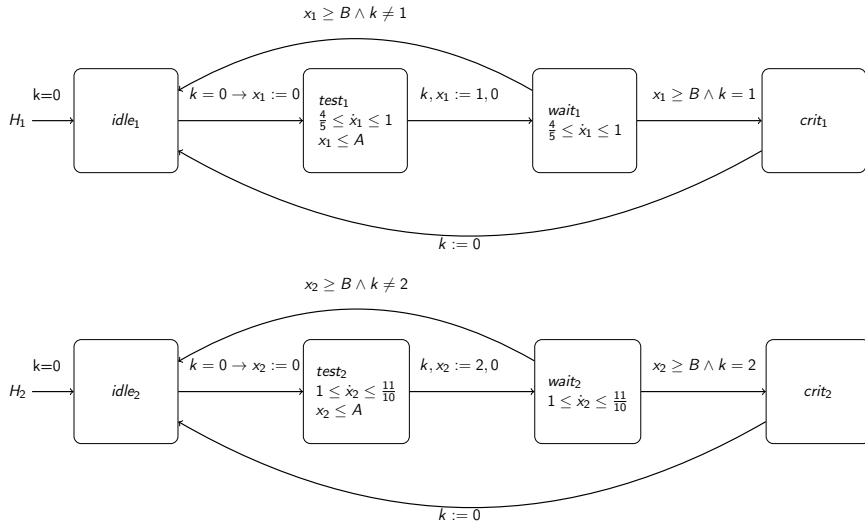
Fischer's mutual exclusion protocol

There are also more complex systems we can deal with similarly...



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...but now we focus of Kripke structures and discrete transition systems.

Early 1980s: First implementations of **model checking** as verification technique

- **Explicit** representations of the transition graphs
- **Problem:** Due to the **state space explosion** not applicable for complex industrial settings

1990: **Symbolic** model checking

- **BDDs** represent *characteristic functions* of state sets symbolically
- **Problem:** Building the BDD may be **expensive**

1999: **Bounded** model checking [*Biere et al.*]

- Check the existence of finite paths of incremental length by a **SAT- or SMT-solver**
- **Problem:** Incomplete (in general)
- Works for different logics, we consider only **reachability**

- Given a model M and a logical description φ of safe states (invariant), a **counterexample** is a **finite path of M reaching a non- φ state**.
- If a system is buggy, counterexamples are extremely important for detecting and fixing the error.
- **Bounded model checking (BMC)** is a technique to **search for finite counterexamples** (also for more complex models and logics).

Algorithm:

- 1 Set $k = 0$
- 2 Construct a logical formula BMC_k describing a finite path
 - through the underlying model
 - of length k ,
 - and violating a safety property φ after k steps.
- 3 Check BMC_k for satisfiability using a SAT or SMT solver
- 4 If SAT, the resulting assignment describes a counterexample → terminate
- 5 If UNSAT, increment k , goto 1.

$$BMC_k = I(s_0) \wedge T(s_0, s_1) \wedge \dots \wedge T(s_{k-1}, s_k) \wedge \neg\varphi(s_k)$$

- I and T are (nearly) straightforward for Kripke structures and timeless transition systems
- $I(s_0) \wedge \bigwedge_{i=0}^{k-1} T(s_i, s_{i+1})$ is called the **unfolding of the transition relation**

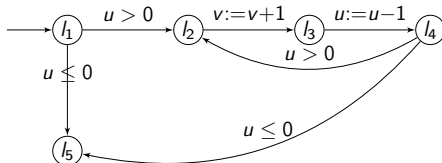
BMC example

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Safety property: Return value = sum of the input values

BMC example

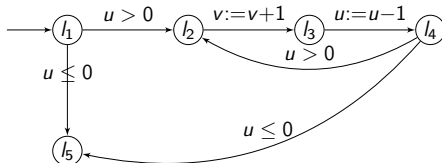
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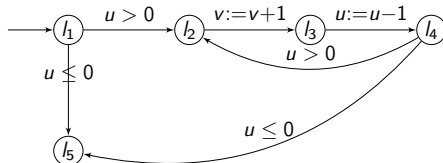


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BMC for counterexamples: (encoding location l_j in step i by $at_i = j$)

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$I(i)$: $at_i = 1$

$T_{1,2}(i, i+1)$: $at_i = 1 \wedge at_{i+1} = 2 \wedge u_i > 0 \wedge u_{i+1} = u_i \wedge v_{i+1} = v_i$

$T_{1,5}(i, i+1)$: $at_i = 1 \wedge at_{i+1} = 5 \wedge u_i \leq 0 \wedge u_{i+1} = u_i \wedge v_{i+1} = v_i$

...

$T(i, i+1)$: $T_{1,2}(i, i+1) \vee T_{1,5}(i, i+1) \vee \dots$

$\varphi(i)$: $at_i \neq 5 \vee v_i = v_0 + u_0$

BMC_0 : $I(0) \wedge \neg\varphi(0)$ UNSAT

BMC_0 : $I(0) \wedge T(0, 1) \wedge \neg\varphi(1)$ SAT

- Termination is guaranteed iff counterexample exists
- If no counterexample exists, procedure does not terminate
- Upper bound for k to ensure property: **Completeness threshold**
- Another possibility to make BMC complete: **k-induction** (not content of this lecture)

Completeness threshold

- For each (finite state) system M , property p and given translation scheme there exists a number CT , called **completeness threshold**.
- For reachability properties, CT is equal to the **reachability diameter**, i.e., the minimal distance required to reach all (reachable) states of the system.

Completeness threshold

- For each (finite state) system M , property p and given translation scheme there exists a number \mathcal{CT} , called **completeness threshold**.
- For reachability properties, \mathcal{CT} is equal to the **reachability diameter**, i.e., the minimal distance required to reach all (reachable) states of the system.

Definition (Reachability Diameter)

$$rd(M) := \min \left\{ i \mid \forall n > i. \forall s_0, \dots, s_n. \exists t \leq i. \exists s'_0, \dots, s'_t. \right.$$

$$\left. \left(I(s_0) \wedge \bigwedge_{j=0}^{n-1} T(s_j, s_{j+1}) \right) \rightarrow \left(I(s'_0) \wedge \bigwedge_{j=0}^{t-1} T(s'_j, s'_{j+1}) \wedge s'_t = s_n \right) \right\}$$

“Every state that is reachable in n steps, is also reachable in i steps.”

- This yields maximal shortest paths in the system.

Completeness threshold

- **Problem:** Show the property **for all** n
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“Every state that is reachable in $i + 1$ steps, is also reachable in at most i steps.”

- **Problem:** Formula contains **quantifier alternation**

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- **Solution:** **Over-approximation of $rd(M)$**

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Definition (Recurrence Diameter)

$rdr(M) :=$

$$\max \left\{ i \mid \exists s_0 \dots s_i : I(s_0) \wedge \bigwedge_{j=0}^{i-1} T(s_j, s_{j+1}) \wedge \bigwedge_{j=0}^{i-1} \bigwedge_{k=j+1}^i s_j \neq s_k \right\}$$

“Longest loop-free initial path in M .”

- As every shortest path is a loop-free path, this is an over-approximation of $rd(M)$.

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Deductive verification: Example

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