Safely:
$$\forall \overline{\Pi}$$
 $[\forall i, \overline{\exists} \overline{\Pi}', \overline{\Pi}(i), \overline{\Pi}' \models \Psi] = \rangle \overline{\Pi} \models \Psi$
linears: $\{\overline{\Pi}(i) \mid \overline{\Pi} \notin \operatorname{Paths}_{in} \land \overline{\Pi} \models \Psi\} = \operatorname{Paths}_{i} \xrightarrow{d_{i-1}}$

Aga GFa = violatie finite infinite satisfi infinite infinite

Modeling and Analysis of Hybrid Systems Model checking timed automata

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Basic method: Abstraction

- Given: a concrete system (here: a timed automaton)
- Goal: reduce the size of the system (here: reduce the infinite state space to a finite one)
- Result: abstract system

(here: region transition system)



Basic method: Abstraction

- Given: a concrete system
 - (here: a timed automaton)
- Goal: reduce the size of the system (here: reduce the infinite state space to a finite one)
- Result: abstract system (here: region transition system)
- Conservative (safe) abstraction: If we see both the concrete and the abstract system as black boxes and make experiments with them, we cannot distinguish between their observable behavior.
- Two systems P and P' have the same observable behaviour iff for each context C we have that [[C[P]]] = [[C[P']]]. (C[P]: the composition of C and P, [.]: (global) semantics)
- E.g., for programs it could mean the same input-output behaviour.
 For model checking we require that they satisfy the same formulas of the underlying logic.

(here: TCTL)

Input:timed automaton \mathcal{T} , TCTL formula ψ Output:the answer to the question if $\mathcal{T} \models \psi$

- **1** Eliminate the timing parameters from ψ , resulting in $\hat{\psi}$;
- 2 Make a finite abstraction of the state space
- 3 Construct abstract transition system RTS with $\mathcal{T} \models \psi$ iff $RTS \models \hat{\psi}$.
- **4** Apply CTL model checking to check whether $RTS \models \hat{\psi}$;
- **5** Return the model checking result.



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$$\begin{array}{c} \sigma & \models_{\mathcal{T}C\mathcal{T}L} & \mathbf{E}(\psi_1 & \mathcal{U}^J & \psi_2) \text{ iff} \\ \mathsf{reset}(z) \text{ in } \sigma & \models_{\mathcal{T}C\mathcal{T}L} & \mathbf{E}(\psi_1 & \mathcal{U} & ((z \in J) \land \psi_2) \, . \\ & & \models_{\mathcal{C}\mathcal{T}L} \end{array}$$

•

For any state σ of ${\mathcal T}$ it holds that

•

•

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(1) a E A P $(l_0, v) \models a \quad (l_1, p') \not\models a \quad = > \quad (L, v) \approx (l', v') = > (= l')$ $(1) \times \sim c =) \times (1)$ $C \in C_{X}$ $(\ell_{1}\nu) \approx (\ell_{1}'\nu') \Longrightarrow \nu(x) \le 2$ iff V'(x) 62 =) x = = $\forall x \in C$. $\lfloor P(x) \rfloor = \lfloor V'(x) \rfloor$ (x_1) frac(x) (12) $(e_1, x=0.4, y=0.5)(e_1, x=y=0.7)(e_1, x=0.5, y=0.7)$



Keywords: Finite abstraction Equivalence relation, equivalence classes Bisimulation

And what does it mean in our context?

We search for an equivalence relation \cong on states, such that equivalent states satisfy the same (sub)formulae ψ' occurring in the timed automaton \mathcal{T} or in the specification ψ :

$$\sigma \cong \sigma' \quad \Rightarrow \quad \left(\sigma \models \psi' \quad \textit{iff} \quad \sigma' \models \psi'\right).$$

Since the set of such (sub)formulae is finite, we strive for a finite number of equivalence classes.

Definition

Let $LSTS = (\Sigma, Lab, Edge, Init)$ be a state transition system, AP a set of atomic propositions, and $L : \Sigma \to 2^{AP}$ a labeling function over AP. A bisimulation for LSTS is an equivalence relation $\approx \subseteq \Sigma \times \Sigma$ such that for all $\sigma_1 \approx \sigma_2$

$$L(\sigma_1) = L(\sigma_2)$$

2 for all $\sigma'_1 \in \Sigma$ with $\sigma_1 \xrightarrow{a} \sigma'_1$ there exists $\sigma'_2 \in \Sigma$ such that $\sigma_2 \xrightarrow{a} \sigma'_2$ and $\sigma'_1 \approx \sigma'_2$.





Definition

Let $\mathcal{T} = (Loc, \mathcal{C}, Lab, Edge, Inv, Init)$ be a timed automaton, AP a set of atomic propositions, and $L : \Sigma \to 2^{AP}$.

A time abstract bisimulation on \mathcal{T} is an equivalence relation $\approx \subseteq \Sigma \times \Sigma$ such that for all $\sigma_1, \sigma_2 \in \Sigma$ satisfying $\sigma_1 \approx \sigma_2$

- $L(\sigma_1) = L(\sigma_2)$
 - for all $\sigma'_1 \in \Sigma$ with $\sigma_1 \xrightarrow{a} \sigma'_1$ there is a $\sigma'_2 \in \Sigma$ such that $\sigma_2 \xrightarrow{a} \sigma'_2$ and $\sigma'_1 \approx \sigma'_2$
 - for all $\sigma'_1 \in \Sigma$ with $\sigma_1 \xrightarrow{t_1} \sigma'_1$ there is a $\sigma'_2 \in \Sigma$ such that $\sigma_2 \xrightarrow{t_2} \sigma'_2$ and $\sigma'_1 \approx \sigma'_2$.

Lemma

Assume a timed automaton \mathcal{T} with state space Σ , and a time-abstract bisimulation $\approx \subseteq \Sigma \times \Sigma$ on \mathcal{T} . Then for all $\sigma, \sigma' \in \Sigma$ with $\sigma \approx \sigma'$ we have that for each path

$$\pi: \sigma \xrightarrow{\alpha_1} \sigma_1 \xrightarrow{\alpha_2} \sigma_2 \xrightarrow{\alpha_3} \dots$$

of \mathcal{T} there exists a path

$$\pi': \sigma' \stackrel{\alpha_1'}{\to} \sigma_1' \stackrel{\alpha_2'}{\to} \sigma_2' \stackrel{\alpha_3'}{\to} \dots$$

of ${\mathcal T}$ such that for all i

• $\sigma_i \approx \sigma'_i$, • $\alpha_i = \alpha'_i$ if $\alpha_i \in Lab$ and • $\alpha_i, \alpha'_i \in \mathbb{R}_{>0}$ otherwise. Now, back to timed automata. How could such a bisimulation look like?

Since, in general,

- the atomic propositions assigned to and
- the paths starting at

different locations in \mathcal{T} are different, only states (l, ν) and (l', ν') satisfying l = l' should be equivalent.

Equivalent states should satisfy the same atomic clock constraints. Notation:

- Integral part of $r \in \mathbb{R}$: $\lfloor r \rfloor = \max \{ c \in \mathbb{N} \mid c \leq r \}$
- Fractional part of $r \in \mathbb{R}$: $frac(r) = r \lfloor r \rfloor$

For clock constraints x < c with $c \in \mathbb{N}$ we have:

$$\nu \models x < c \Leftrightarrow \nu(x) < c \Leftrightarrow \lfloor \nu(x) \rfloor < c.$$

For clock constraints $x \leq c$ with $c \in \mathbb{N}$ we have: $\begin{array}{c} v \models x \leq c \\ (v(x) = c \\ (v(x) \mid c)))))))))$

I.e., only states (l,ν) and (l,ν') satisfying

 $\lfloor \nu(x) \rfloor = \lfloor \nu'(x) \rfloor$ and $frac(\nu(x)) = 0$ iff $frac(\nu'(x)) = 0$

for all $x \in C$ should be equivalent.

 $x \leq c$ $(t, v) \approx (t, v')$

Problem: It would generate infinitely many equivalence classes!

Let c_x be the largest constant which a clock x is compared to in \mathcal{T} or in ψ . Then there is no observation which could distinguish between the x-values in (l, ν) and (l, ν') if $\nu(x) > c_x$ and $\nu'(x) > c_x$. I.e., only states (l, ν) and (l, ν') satisfying

$$\frac{(\nu(x) > c_x \land \nu'(x) > c_x) \quad \lor}{\left(\left\lfloor \nu(x) \right\rfloor = \left\lfloor \nu'(x) \right\rfloor \land \operatorname{frac}(\nu(x)) = 0 \text{ iff } \operatorname{frac}(\nu'(x)) = 0 \right)}$$

for all $x \in C$ should be equivalent.



As the following example illustrates, we must make a further refinement of the abstraction, since it does not distinguish between states satisfying different formulae.



What we need is a refinement taking the order of the fractional parts of the clock values into account. However, again only for values below the largest constants to which the clocks get compared. I.e., only states (l, ν) and (l, ν') satisfying

 $\begin{aligned} (\nu(x),\nu'(x) > c_x \wedge \nu(y),\nu'(y) > c_x) & \lor \\ (frac(\nu(x)) \leq frac(\nu(y)) & iff \quad frac(\nu'(x)) \leq frac(\nu'(y)) \wedge \\ frac(\nu(x)) & \cong frac(\nu(y)) & iff \quad frac(\nu'(x)) \equiv frac(\nu'(y)) \wedge \\ frac(\nu(x)) & \geqslant frac(\nu(y)) & iff \quad frac(\nu'(x)) > frac(\nu'(y))) \end{aligned}$

for all $x, y \in C$ should be equivalent. Because of symmetry the following is also sufficient:

$$\begin{array}{l} (\nu(x),\nu'(x) > \overbrace{c_x} \land \nu(y),\nu'(y) > c_y) \quad \lor \\ (frac(\nu(x)) \leq frac(\nu(y)) \quad iff \quad frac(\nu'(x)) \leq frac(\nu'(y))) \end{array}$$

for all $x, y \in \mathcal{C}$ should be equivalent.



Definition

For a timed automaton \mathcal{T} and a TCTL formula ψ , both over a clock set \mathcal{C} , we define the clock equivalence relation $\cong \subseteq \Sigma \times \Sigma$ by $(l, \nu) \cong (l', \nu')$ iff l = l' and

$$\lfloor \nu(x) \rfloor = \lfloor \nu'(x) \rfloor \land (frac(\nu(x)) = 0 \quad iff \quad frac(\nu'(x)) = 0)$$

 \blacksquare for all $x,y\in \mathcal{C}$ if $\nu(x),\nu'(x)\leq c_x$ and $\nu(y),\nu'(y)\leq c_y$ then

 $\operatorname{frac}(\nu(x)) \leq \operatorname{frac}(\nu(y)) \quad \operatorname{iff} \quad \operatorname{frac}(\nu'(x)) \leq \operatorname{frac}(\nu'(y)).$

The clock region of an evaluation $\nu \in V$ is the set $[\nu] = \{\nu' \in V \mid \nu \cong \nu'\}$. The clock region of a state $\sigma = (l, \nu) \in \Sigma$ is the set $[\sigma] = \{(l, \nu') \in \Sigma \mid \nu \cong \nu'\}$.
Lemma

Clock equivalence is a bisimulation over $AP' = AP \cup ACC(\mathcal{T}) \cup ACC(\psi)$.







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We have defined regions as abstract states, now we connect them by abstract transitions.

Two kinds of transitions: time and discrete

Definition

The clock region $[r_{\infty}] = \{\nu \in V \mid \forall x \in C. \ \nu(x) > c_x\}$ is called unbounded. Let r, r' be two clock regions. The region r' is the successor clock region of r, denoted by $\underline{r'} = succ(r)$, if either

• $r = r' = r_{\infty}$, or • $r \neq r_{\infty}$, $r \neq r'$, and for all $\nu \in r$:

 $\exists d \in \mathbb{R}_{>0}. \ (\nu + d \in r' \ \land \ \forall 0 \le d' \le d. \ \nu + d' \in r \cup r').$

The successor state region is defined as succ((l, r)) = (l, succ(r)).







Definition

Let $\mathcal{T} = (Loc, \mathcal{C}, Lab, Edge, Inv, Init)$ be a non-zeno timelock-free timed automaton with an atomic proposition set AP and a labeling function L, and let $\hat{\psi}$ be an unbounded TCTL formula over \mathcal{C} and AP. The region transition system of \mathcal{T} for $\hat{\psi}$ is a labelled state transition system $\mathcal{RTS}(\mathcal{T}, \psi) = (\Sigma', Lab', Edge', Init')$ with atomic propositions AP' and a labeling function L' such that

•
$$\Sigma'$$
 the finite set of all state regions $((\ell, [\nu])) | \nu \models \Im (\ell)$

$$\blacksquare Init' = \{ [\sigma] \mid \sigma \in Init \}$$

$$\underline{AP'} = \underline{AP} \cup \underline{ACC}(\mathcal{T}) \cup \underline{ACC}(\hat{\psi})$$

$$L'((l,r)) = L(l) \cup \{g \in AP' \backslash AP \mid r \models g\}$$

and

Definition

$$(l, a, (g, C), l') \in Edge$$

$$(\ell, \nu) \stackrel{\bullet}{\longrightarrow} (\ell', \nu') \stackrel{\text{for some}}{\longrightarrow} (\ell', \nu') \stackrel{\text{for some}}{\longrightarrow} (\ell', \nu') \stackrel{\text{for some}}{\longrightarrow} (\ell', \nu')$$

$$(r \models g \quad r' = reset(C) \text{ in } r \quad r' \models Inv(l') \\ \hline (l, r) \stackrel{a}{\rightarrow} (l', r')$$

$$(r \models Inv(l)) \quad \underline{succ(r)} \models Inv(l) \\ \hline (l, r) \stackrel{t}{\rightarrow} (l, succ(r))$$
Rule Time











Lemma

For non-zeno \mathcal{T} and $\pi = s_0 \rightarrow s_1 \rightarrow \ldots$ an initial, infinite path of \mathcal{T} :

- if π is time-convergent, then there is an index j and a state region (l,r) such that $s_i \in (l,r)$ for all $i \geq j$.
- if there is a state region (l,r) with $r \neq r_{\infty}$ and an index j such that $s_i \in (l,r)$ for all $i \geq j$ then π is time-convergent.

Lemma

For a non-zeno timed automaton T and a TCLT formula ψ :

$$\mathcal{T} \models_{\textit{TCTL}} \psi \quad \textit{iff} \quad \textit{RTS}(\mathcal{T}, \hat{\psi}) \models_{\textit{CTL}} \hat{\psi}$$



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The procedure is quite similar to CTL model checking for finite automata.

One difference:

Handling nested time bounds in TCTL formulae



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